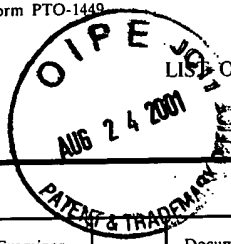


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April 13, 1998

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U.S. PATENT DOCUMENTS

*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
MT	AA 5,552,329	9-3-96	Kim et al.			
	AB					
	AC					
	AD					
	AE					
	AF					
	AG					
	AH					
	AI					
	AJ					
	AK					

FOREIGN PATENT DOCUMENTS

	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
	AL						
	AM						
	AN						
	AO						
	AP						

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

MT	AR	WOLF, Ph.D., Stanley et al., <u>Silicon Processing for The VLSI Era</u> , Vol. 1, Lattice Press, pp. 216-218, (1986)
MT	AS	WOLF, Ph.D., Stanley et al., <u>Silicon Processing for The VLSI Era</u> , Vol. II, Lattice Press, pp. 66 & 67 (continued) (1990)
	AT	

EXAMINER Michael Trinh

DATE CONSIDERED 11/26/01

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